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**CLIENT: 1772** 

MATTER: 15258US05

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		First Named Inven	tor	Rofougaran	CENTRAL	
		Art Unit		2682	FED	
		Examiner Name		M. Milord	. 28	
(to be used for all correspondence		Attorney Docket N	umber	15258US05		
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Name (Print/type) Michael T. C		Registration No. (A	ttomey/A	gent)	44,636	
Signature Michael	T. Crus			Date	February 14, 2006	

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	• • • •			aminer Name	M. Milord	<u></u>	<del></del>		FEB 1 4 2
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	Michael 1 (	/we/~.		(Attorney/Agent	, , ,		Date		ebruary 14, 2006

## Attorney Docket No. 15258US05

#### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

BEST AVAILARIE COP In the Application of: Serial No: Filed:

09/699,019

A. Rofougaran et al.

October 27, 2000

For: ADAPTIVE RADIO TRANSCEIVER WITH A BAND-

PASS FILTER

Examiner:

M. Milord

Group Art Unit:

2682

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Michael T. Crazz

#### SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Applicants respectfully enclose a PTO Form SB 08A that lists documents that were cited during the prosecution of the grandparent application and parent application to the present application. The grandparent application is United States Patent Application Serial No. 09/634,552, filed August 8, 2000. The parent application is United States Patent Application Serial No. 09/695,715, filed October 23, 2000.

The present application is a continuation of United States Patent Application Serial No. 09/695,715, filed October 23, 2000, which, in turn, is a continuation-in-part of United States Patent Application Serial No. 09/634,552, filed August 8, 2000.

Appl. No. 09/699,019 Supp. IDS dated Feb. 14, 2006

Under M.P.E.P. § 609.02 (Rev. 3, Aug. 2005), copies of the items listed on the accompanying PTO Form SB 08A need not be resubmitted in a continuing application. Therefore, no copies are enclosed.

The filing of a supplemental information disclosure statement shall not be construed to be an admission that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

An information disclosure statement fee of \$180 is believed to be due.

It is respectfully requested that this paper and enclosures be considered by the Examiner. Furthermore, the Examiner is respectfully requested to initial a copy of the enclosed PTO Form SB08A and return an initialed copy to Applicants to indicate consideration of the enclosed listed documents.

The Commissioner is hereby authorized to charge additional fees or credit overpayments to the deposit account of McAndrews, Held & Malloy, Account No. 13-0017.

Date: February 14, 2006

Respectfully submitted,

Reg. No. 44,636

McAndrews, Held & Malloy, Ltd. 500 West Madison Street, 34th Floor Chicago, Illinois 60661 Telephone: (312) 775-8084

Fax: (312) 775-8100

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# SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANTS

(use as many sheets as necessary)

Complete if	Known
Application Number	09/699,019
Filing Date	October 27, 2000
First Named Inventor	A. Rofougaran
Group Art Unit	2682
Examiner Name	M. Milord
Attorney Docket Number	15258US05

			U.S. PATENT DO	CUMENTS	
Examiner Initial*	Cite No. <sup>1</sup>	Document Number  Number-Kind Code 2  (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		4,710,970	12.1987	Wu et al.	
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EXAMINER	DATE CONSIDERED	
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 'Applicant's unique citation designation manber (optional). 'See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. 'I Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). 'For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. Skind of document by the appropriate symbols as indicated on the document under WIPO Standard St. 16 if possible.

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This collection of information is required by 37 CFR 1 97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Irademark Office, P.O. Box 1450 Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. Send TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450. If you need assistance in completing the form, cell 1-800-PTO-9199 (1800-786-9199) and select option 2.

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				Application Number	09/699,019
S	UPPLEME	NTAL INFORM	MATION	Filing Date	October 27, 2000
DISCLOSURE STATEMENT BY APPLICANTS				First Named Inventor	A. Rofcugaran
			AL LIOANIO	Group Art Unit	2682
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Sheet	2	Ŏŧ	2	Attorney Docket Number	15258US05

			EIGN PATENT DO	CUMENTS	
Examiner Initials*	Cite No. 1	Foreign Patent Document  Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>3</sup> (# kno	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages
		EP 0 803 997 A2	10.1997	Kuiri, Tapio	or Relevant Figures Appear
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<b>T</b>		OTHER ART - NON PATENT LITERATURE DOCUMENTS
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published
		A. Rofougaran, "A Single-Chip Spread-Spectrum Wireless Transceiver in CMOS," Final Report, Integrated Circuits & Systems Laboratory Electrical Engineering Department, University of California, Los Angeles, California, 339 pages (1999).
		J. Lee et al., "Design of Spiral Inductors on Silicon Substrates with a Fast Simulator". 4 pages, Electrical Engineering Department, University of California, Los Angeles, California, presented at the European Solid-State Circuits Conference, The Hague, The Netherlands (September 1998).
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EXAMINER	DATE CONSIDERED

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